

## Subjects presentation (First week W1)

TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY
16:00-16:50		FPGA	MCM	EAV
17:10-18:00		ESC	MCC	EEG
18:10-19:00		ADM	ASD	HFEC

## TIMETABLE OF THE 1st SEMESTER (Weeks W2 to W15)

TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY
16:00-16:50	FPGA	MCM	EAV	<b>THURSDAY CLASSES</b> ( Weeks W2 to W13) see table bellow
17:10-18:00	ESC	MCC	EEG	
18:10-19:00	ADM	ASD	HFEC	
19:10-20:00				

### THURSDAY ACTIVITIES DISTRIBUTION

TIMETABLE		W2	W3	W4	W5	W6	W7	W8	W9		W10	W11	W12	W13
16:00-16:50		FPGA	EEG	EAV	MCM		MCC	EEG	HFEC		EAV		HFEC	MCC
17:10-18:00														
18:10-19:00		ESC	HFEC	ASD	HFEC		ADM	FPGA	ASD		ADM		MCM	ESC
19:10-20:00														

<b>EEG</b>	Electrical Energy Generation	<b>FPGA</b>	Design of Digital Systems with FPGA
<b>ESC</b>	Energy Storage and Conversion	<b>HFEC</b>	High Frequency Electronics and Communications
<b>EAV</b>	Electrical Architecture of Vehicles	<b>ASD</b>	Automotive Software Development
<b>MCC</b>	Modelling and Control of Switching Converters	<b>ADM</b>	Automotive Development Methodologies
<b>MCM</b>	Modelling and Control of Electric Motors		

## TIMETABLE OF THE 2nd SEMESTER

TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY
16:00-16:50				<b>On Site PEL and ESCL LABORATORIES</b> (Weeks W1 TO W17) Labs 101, 103, 108 and 110
17:10-18:00			INIT (Online)	
18:10-19:00				
19:10-20:00				

### PEL and ESCL LABORATORIES TIMETABLE

TIMETABLE	W1		W2		W3		W4		W5		W6		W7	W8		W9		W10		W11	W12		W13		W14		W15		W16		W17		
SESSIONS	S1		S2				S3				S4				S5					S6				S7				S8					
TIME \ GROUP	A	B	A	B	A	B	A	B	A	B	A	B		A	B	A	B	A	B		A	B	A	B	A	B	A	B	A	B	A	B	
16:00-16:50	L 1	L 1																															
17:10-18:00	0 3	0 8	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1		L 1	L 1	L 1	L 1	L 1	L 1	L 1		L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1
18:10-19:00	L 1	L 1	0 3	0 8	0 8	0 3	1 0	0 8	0 8	1 0	1 0	0 8		0 8	1 0	1 0	0 8	0 8	0 1			1 0	0 8	0 8	0 0	0 8	0 8	0 0	0 8	0 0	0 8	0 0	0 8
19:10-20:00	0 8	0 3					1			1	1			1	1																		

<b>PEL</b>	Power Electronics Laboratory(On site 1 session every 2 weeks)
<b>ESCL</b>	Embeded System and Communications Laboratory (On site 1 session every 2 weeks)

